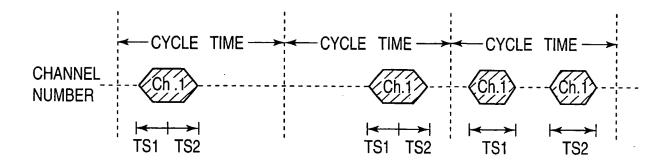
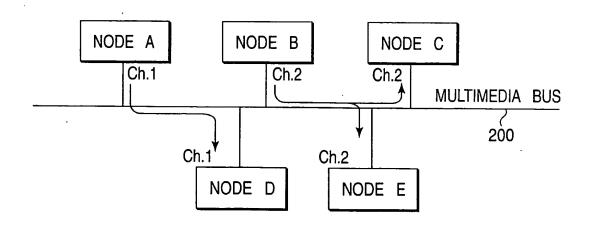


F1G. 1



F I G. 2



F1G.3

CHANNEL CONTROL REGISTER IN CONFIGRATION

Config Add.	Ch Cnt.(8bit)	Ch No.(8bit)	Necessity (16bit)	
10		2	123	
14		3	456	
18		4	789	

مرم مرزي								
7	6	5	4	3	2	1	0	
Ch Ava	In/Out	Reject						

F I G. 4

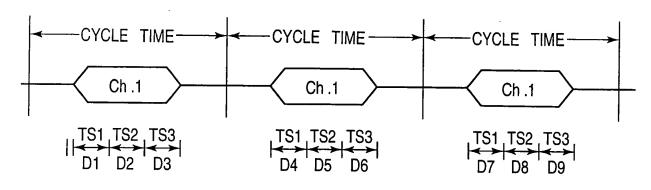
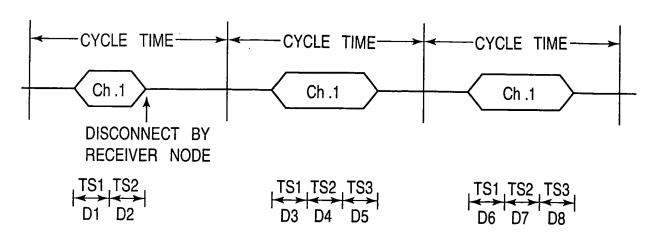
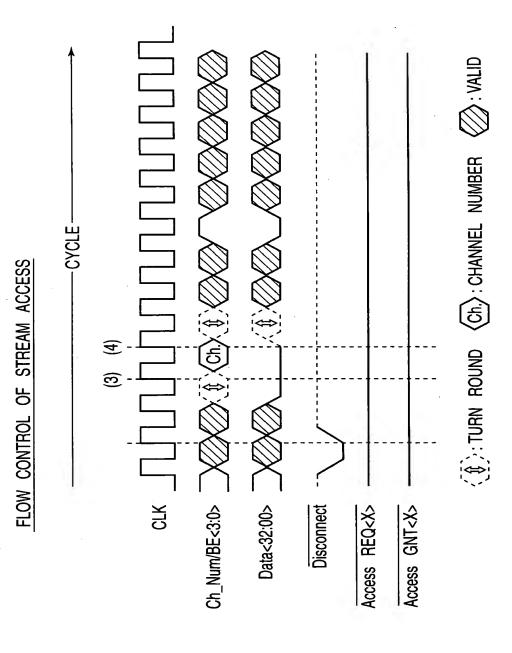


FIG. 5A



F I G. 5B



F1G.6

FIG. 7A

Disconnect

OPEN DRAIN

RECEIVER CIRCUIT

OVERFLOW
DETECTION CIRCUIT

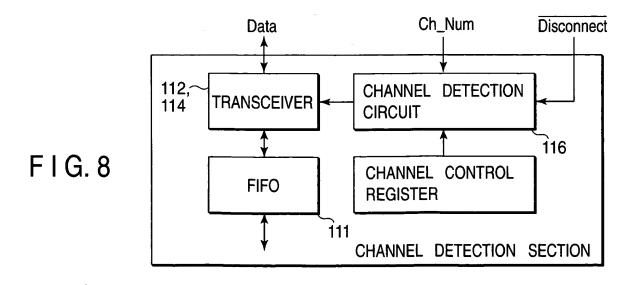
111

RECEIVER SECTION

TRANSMITTER TRANSMISSION STOP CIRCUIT

FIFO

111 TRANSMITTER SECTION



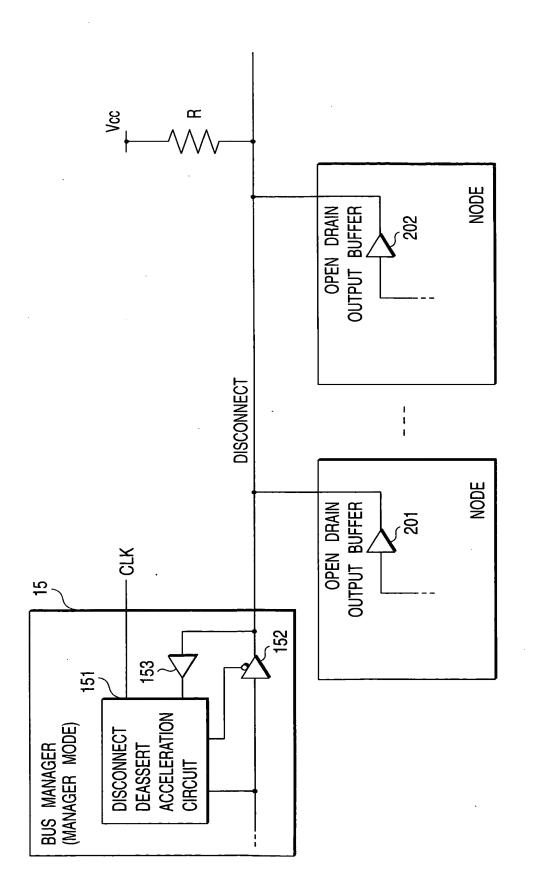


FIG.9

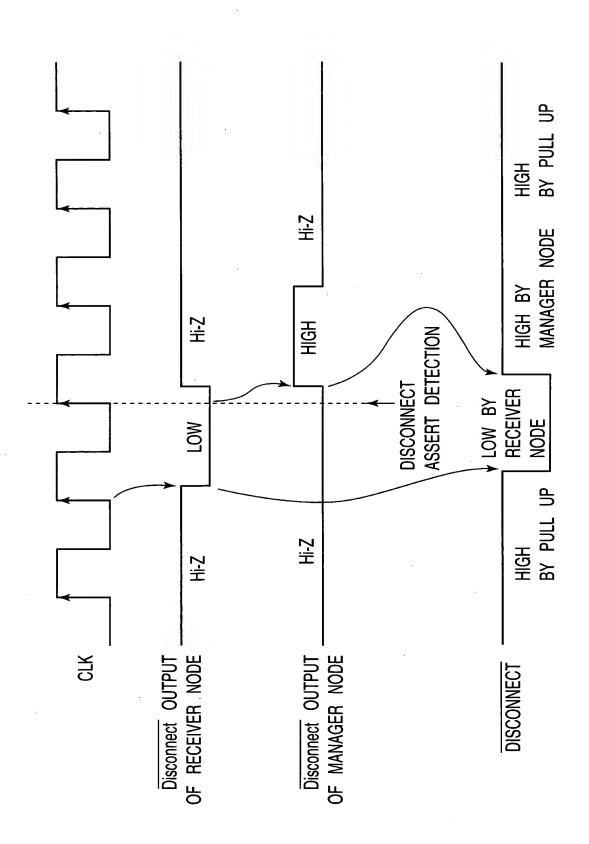


FIG. 10

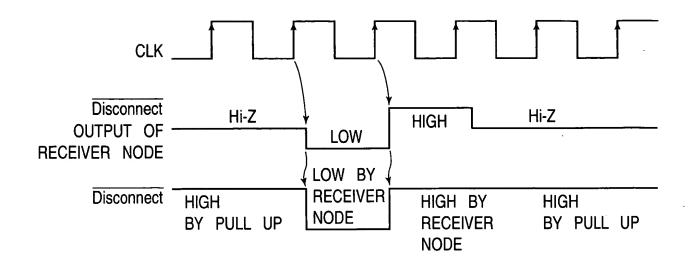
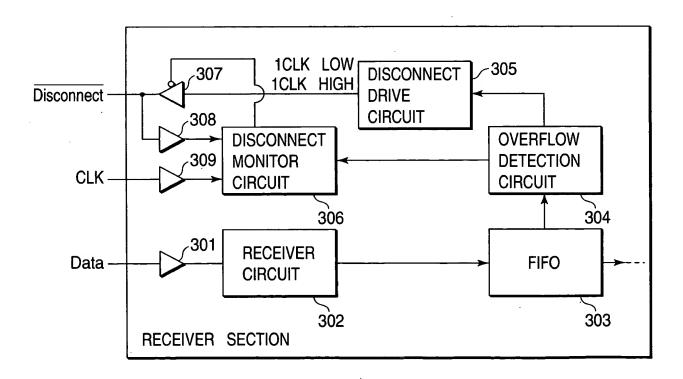


FIG. 11



F I G. 12